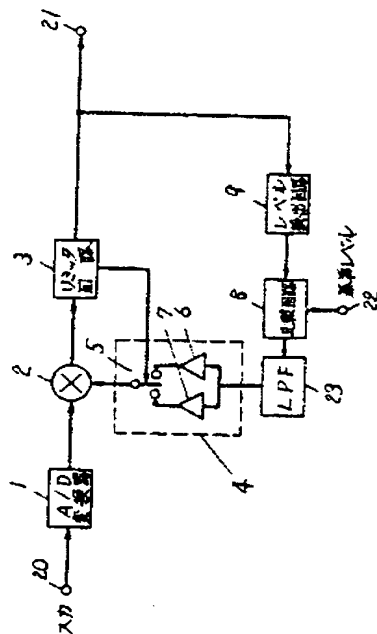


Patent Abstracts of Japan

TITLE : DIGITAL AUTOMATIC GAIN
CONTROLLER



CONSTITUTION: A signal inputted from an input terminal 20 is converted into a digital signal by an A/D converter 1 and the amplitude is controlled by a multiplier 2. The output of the multiplier 2 is given to a limiter circuit 3, the amplitude of the input is limited and outputted to an output terminal 21. The limiter circuit 3 limits the amplitude when the input level reaches a prescribed value or over and switches a coefficient of a variable coefficient circuit 4. On the other hand, the output level of the limiter circuit 3 is detected by a level detection circuit 9 and compared with a reference level inputted from an input terminal 22 at a comparator circuit 8, and the result is fed to the variable coefficient circuit 4. The output of the variable coefficient circuit 4 is fed back to the multiplier 2. As a result, the level at the output terminal 21 is the same as the reference level.

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